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APPLICANTS

Robert Thomas Bailis, Cary, NC;

Charles Edward Kuhlmann, Raleigh, NC;

Charles Steven Lingafelt, Durham, NC; Ann Marie Rincon, Burlington, VT;

** CONTINUING DATA ***** *none PD*

** FOREIGN APPLICATIONS ***** *none PD*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	NC	2	12	3
Verified and Acknowledged	Examiner's Signature <i>Paul</i>	Initials <i>PD</i>		

ADDRESS

47052
 SAWYER LAW GROUP LLP
 PO BOX 51418
 PALO ALTO, CA
 94303

TITLE

Method and system for use of a field programmable gate array (FPGA) cell for controlling access to on-chip functions of a system on a chip (SOC) integrated circuit

FILING FEE RECEIVED	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
		<input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time)